

MPD 1/24/80

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Print Out #3 4/19/80 [Signature]

In re the Application of: Kazuyoshi UENO

Filed : Concurrently herewith

For : SEMICONDUCTOR DEVICE AND A METHOD FOR FORMING A
VIA HOLE IN A SEMICONDUCTOR DEVICE

Serial No.: Concurrently herewith

Assistant Commissioner of Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

S I R:

In order to comply with discretionary regulations 37 CFR 1.97 and 1.98, attached hereto is a copy of Form PTO-1449 and copies of the documents listed thereon. These documents contain information which the Examiner may consider to be important in deciding whether to issue a patent in the instant application.

As one of these documents is written in a language other than English, English language abstract is annexed thereto explaining its relevancy in accordance with 37 CFR §1.98(a)(3).

As this Statement is being filed prior to issuance of a first Office Action, no fee is due.

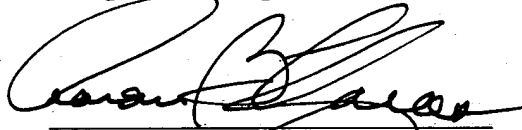
The present Disclosure Statement is being submitted in compliance with 37 CFR 1.56 as an Examiner might consider any cited document important in deciding whether to allow the application to issue as a patent, but the citation of each document is not to be construed as an admission that such document is necessarily relevant or prior art. No representation

is intended that the cited documents represent the results of a complete search, and it is anticipated that the Examiner in the normal course of examination, will make an independent search and will determine the best prior art consistent with 37 CFR 1.104 (a) and 1.106 (b), and in the course of such search will review for relevance every document cited on the attached form even if not initialed.

Early and favorable consideration is respectfully solicited.

Any fee due with this paper, not fully covered by an enclosed check, may be charged on Deposit Account 08-1634.

Respectfully submitted,




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